UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,196,132 B2 Page 1 of 1

APPLICATION NO.: 10/680198
DATED: March 27, 2007
INVENTOR(S): Hideo Ukuda

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

IN THE DRAWINGS:

Sheet No. 13, Figure 14, "SILYCON" should read --SILICON--.

COLUMN 7

Line 7, "
$$n^2+T\chi^1+(1-7)\chi^2=1+T(n_1^2-1)+(1-T)(n_2^2-1)$$
 (Eq. 3)" should read $-n^2=1+T\chi^1+(1-T)\chi^2=1+T(n_1^2-1)+(1-T)(n_2^2-1)$ (Eq. 3)—.

COLUMN 24:

Line 35, " n_d -6.667x10⁻³ v_d +1.70." should read -- n_d \u22e4-6.667x10⁻³ v_d +1.70.--.

Line 64, " $n_d \le 6.667 \times 10^{-3} v_d + 1.70$." should read $-n_d \le -6.667 \times 10^{-3} v_d + 1.70$.--.

Signed and Sealed this

First Day of June, 2010

David J. Kappos Director of the United States Patent and Trademark Office